

FIG. 1A

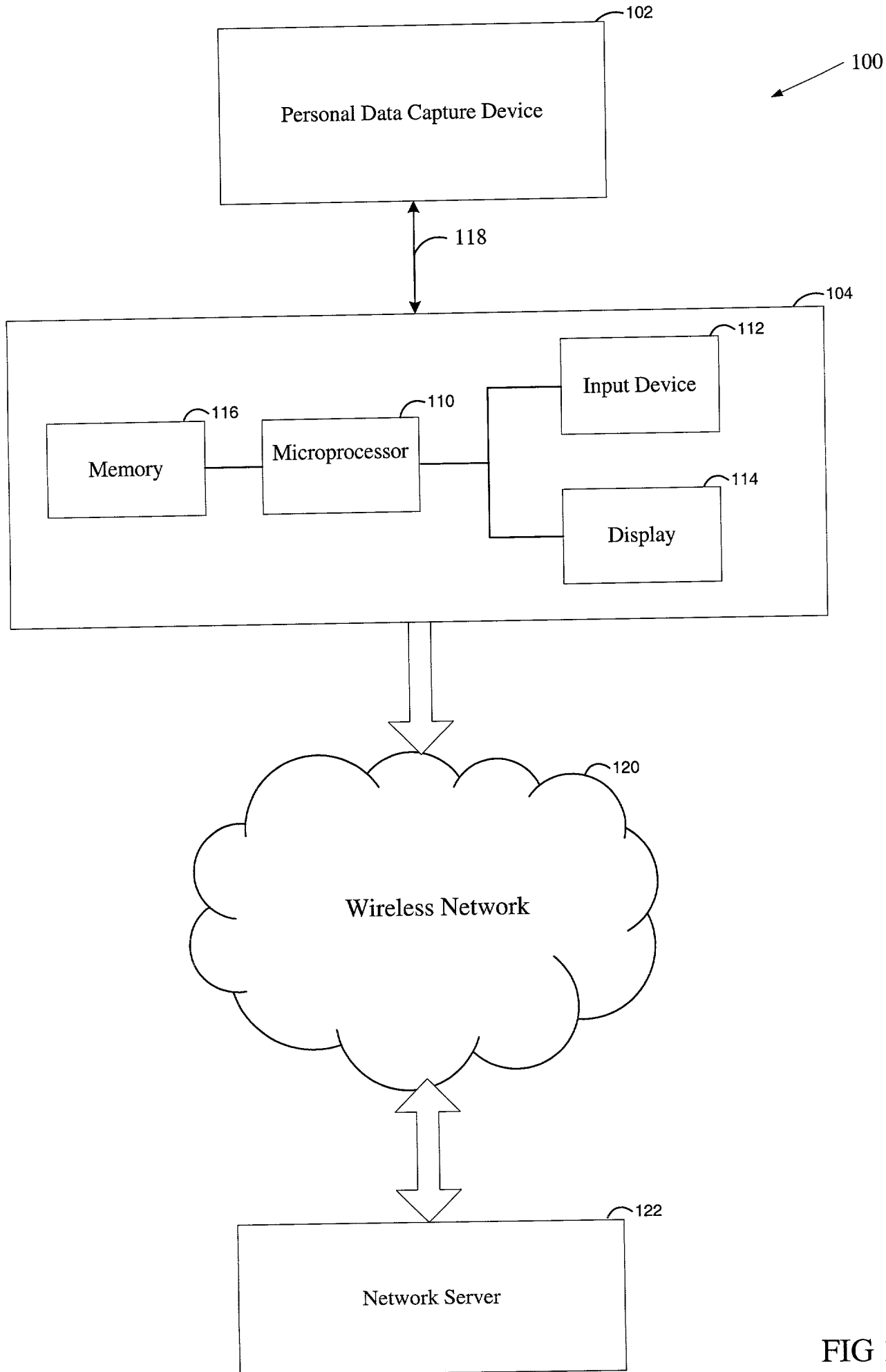


FIG 1B

FIG. 1C is a block diagram of a system 100. The system 100 includes a microprocessor 110, memory 116, software 282, an input device 112, a display 114, a personal parameter receiver 225, a heart rate receiver 240, a motion sensor 250, a GPS receiver 230, a wireless network 120, and a network server 122. The microprocessor 110 is connected to the memory 116, software 282, input device 112, display 114, personal parameter receiver 225, heart rate receiver 240, motion sensor 250, and GPS receiver 230. The microprocessor 110 is also connected to the wireless network 120, which is connected to the network server 122.

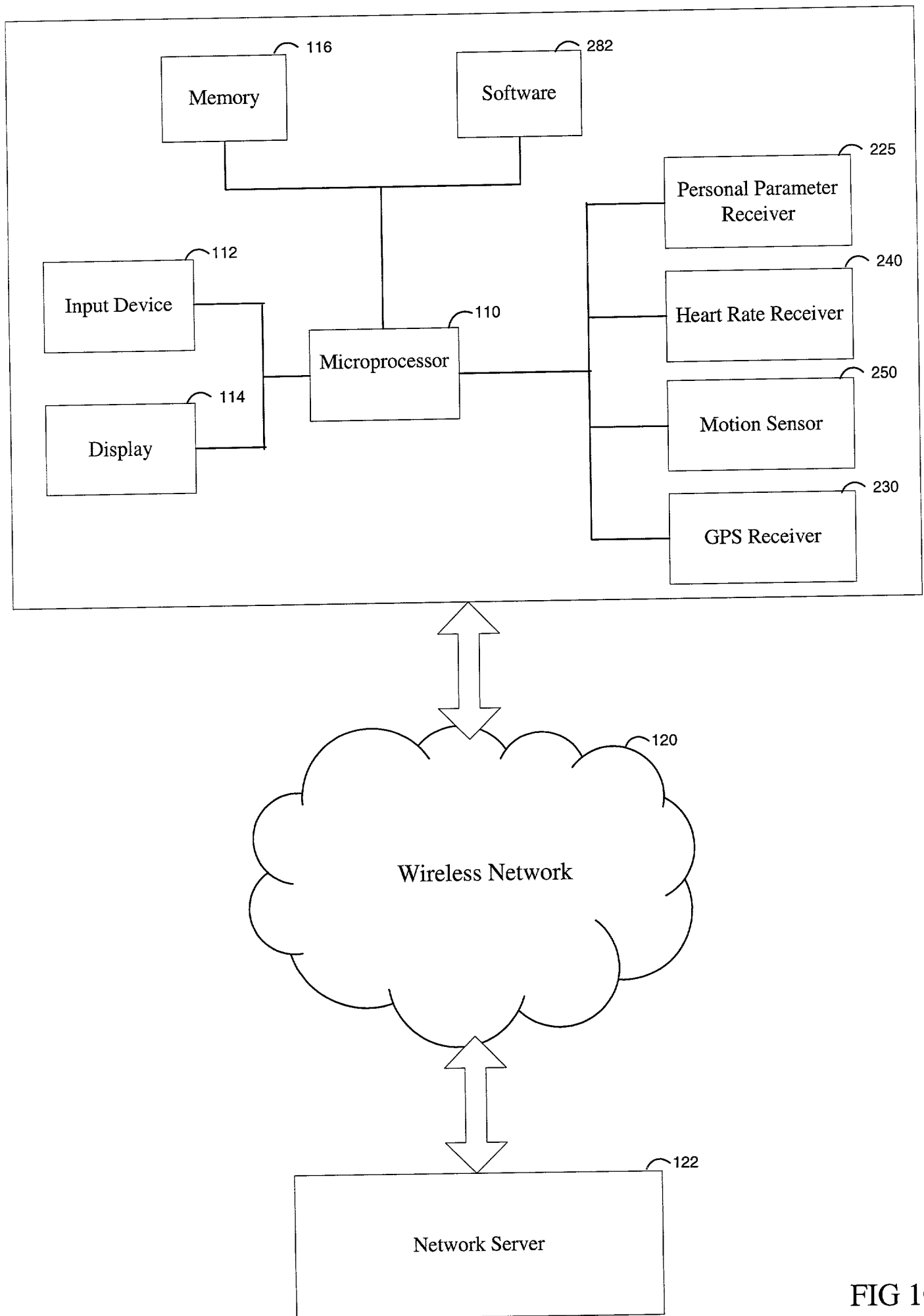


FIG 1C

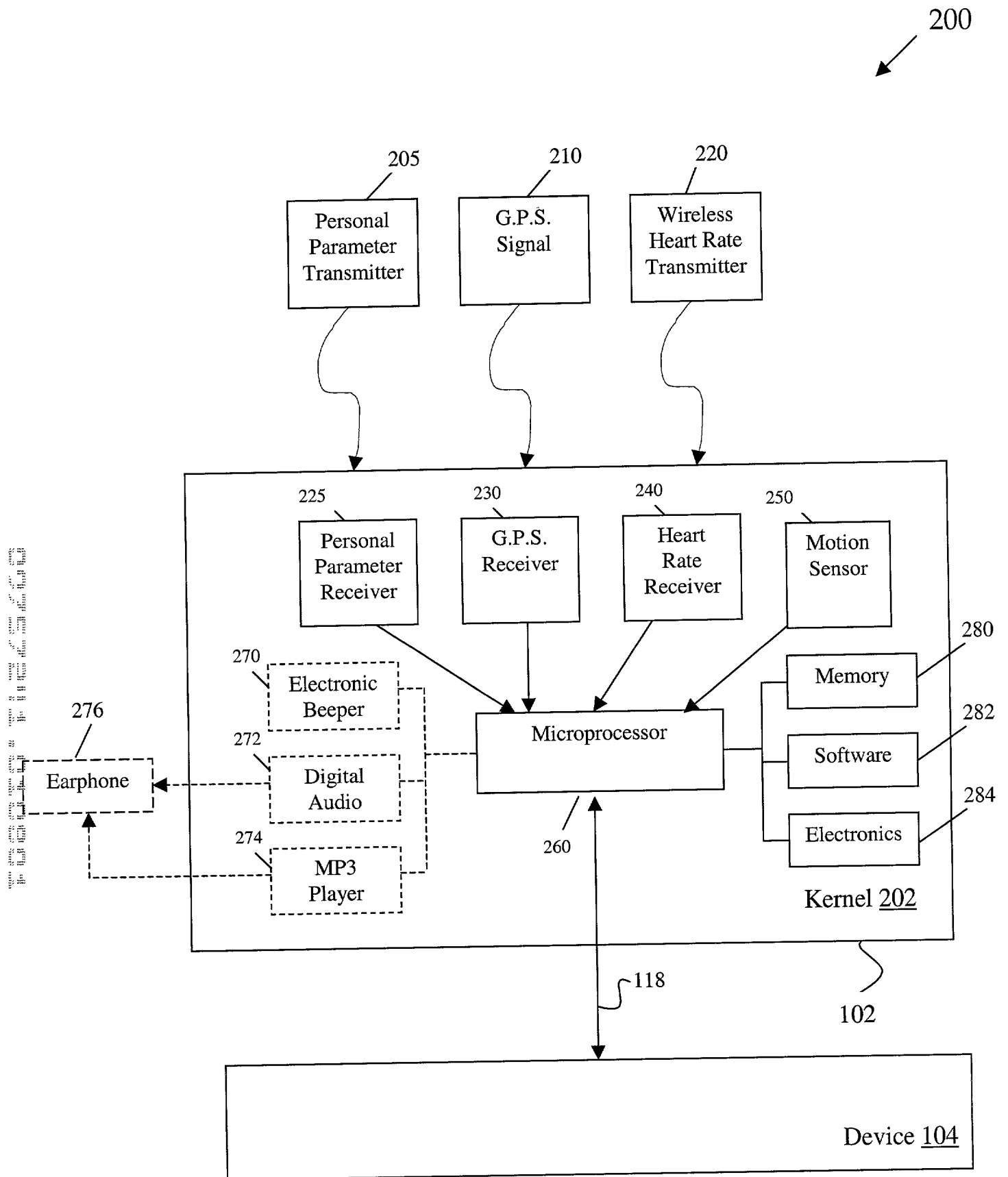
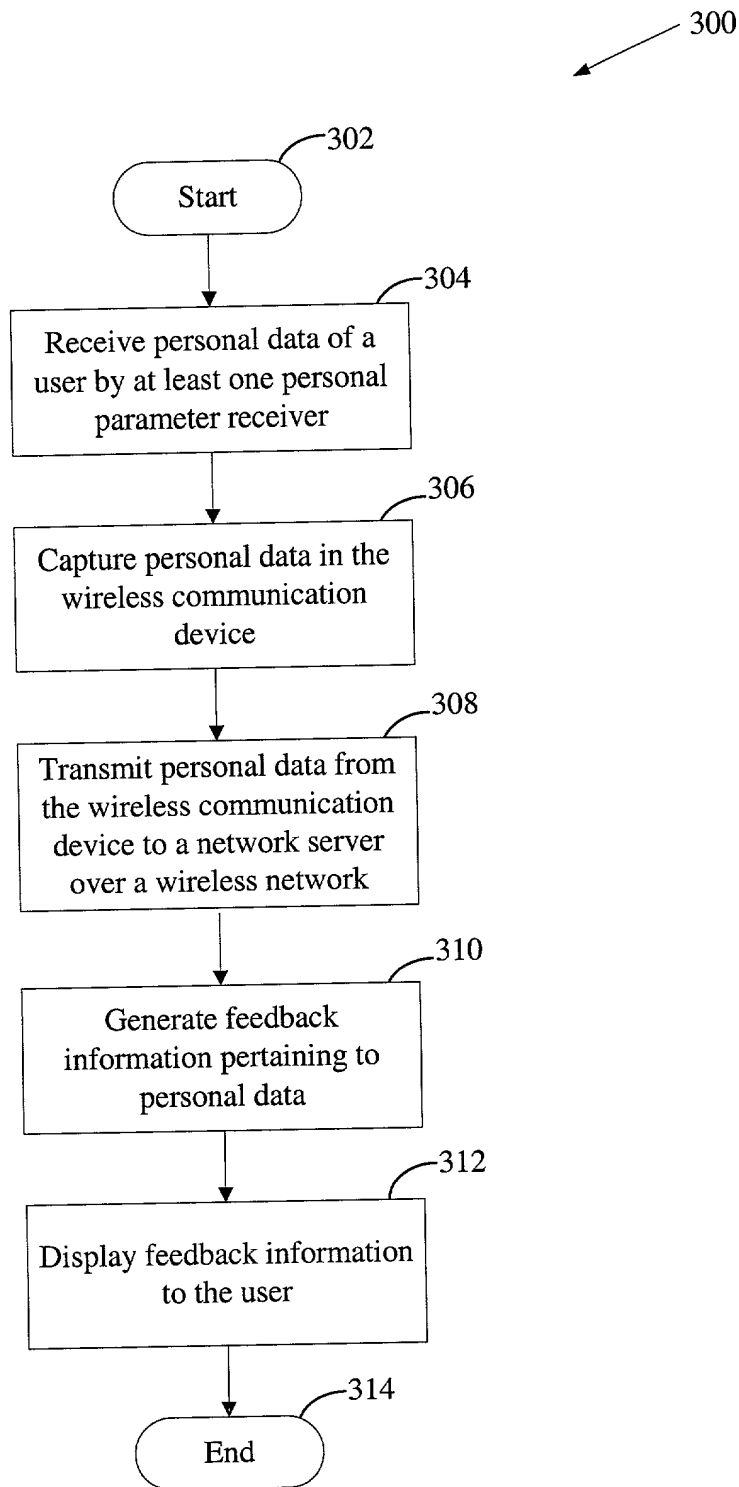


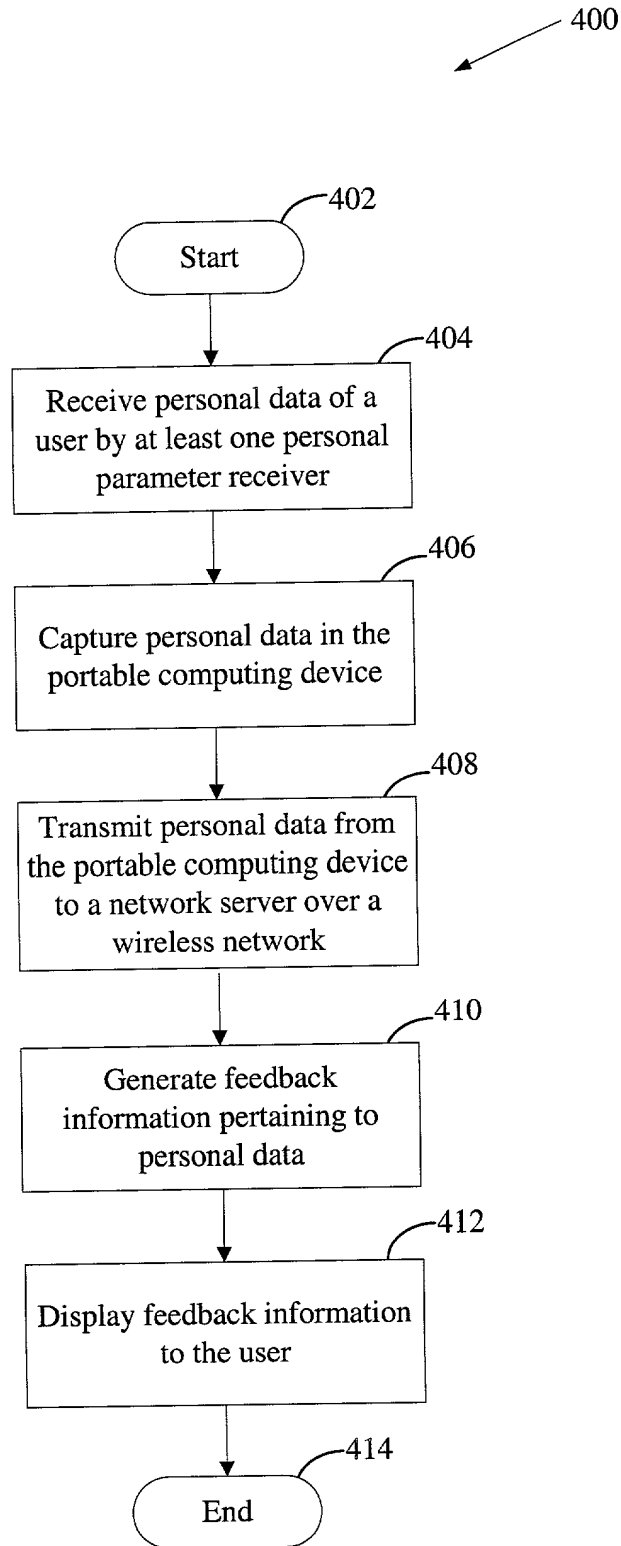
FIG. 2

FIG. 3 is a flowchart illustrating a process for receiving, capturing, transmitting, generating feedback information, and displaying feedback information to a user.



**FIG. 3**

FIG. 4 is a flowchart illustrating a process for receiving, capturing, transmitting, generating feedback information, and displaying feedback information to a user.



**FIG. 4**

500

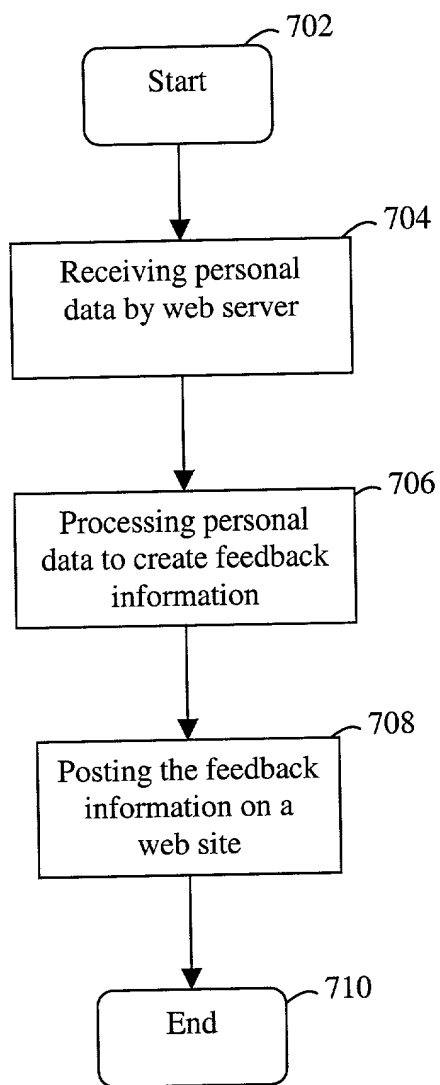


FIG. 5